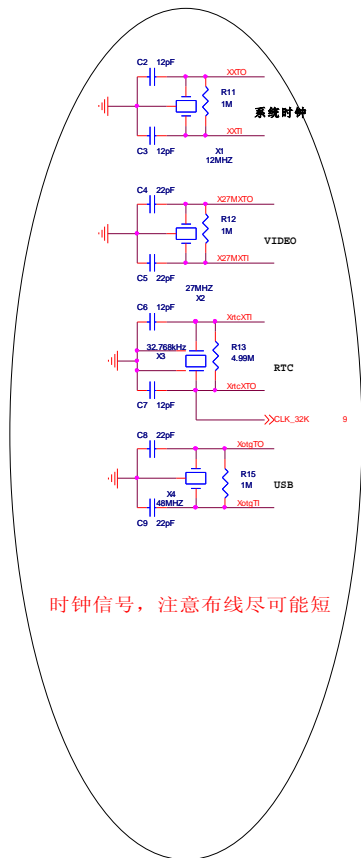
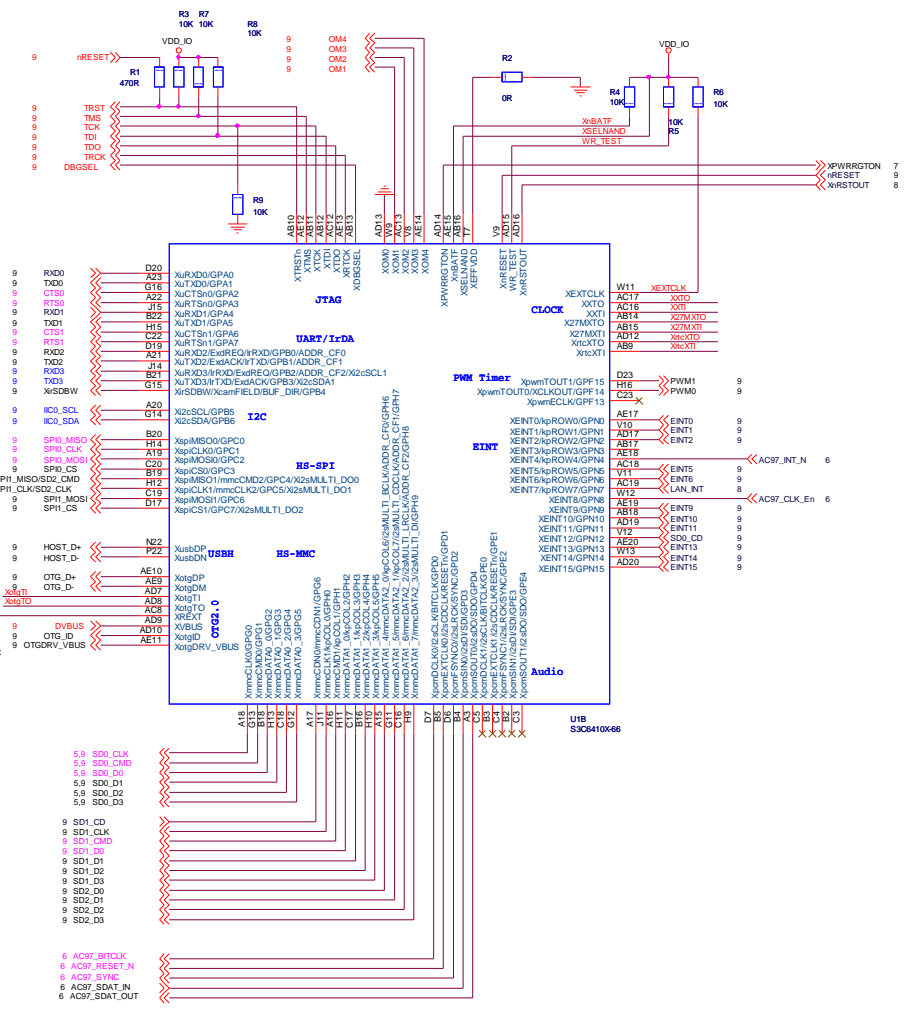


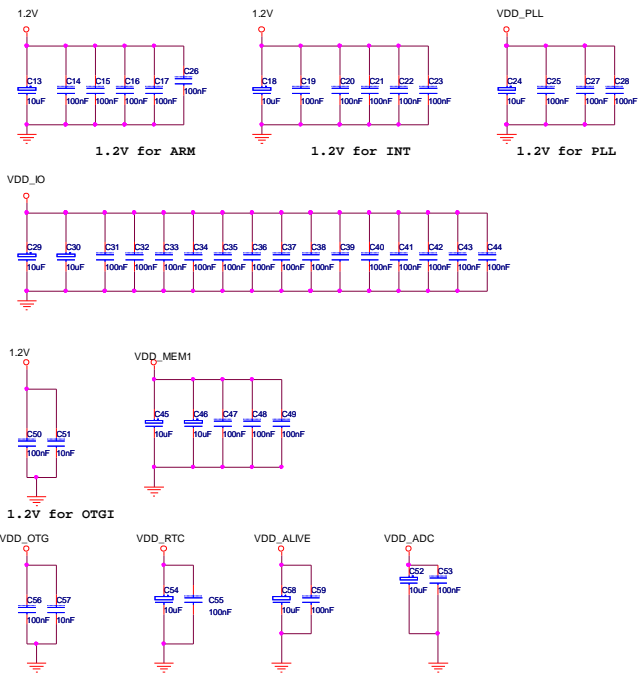
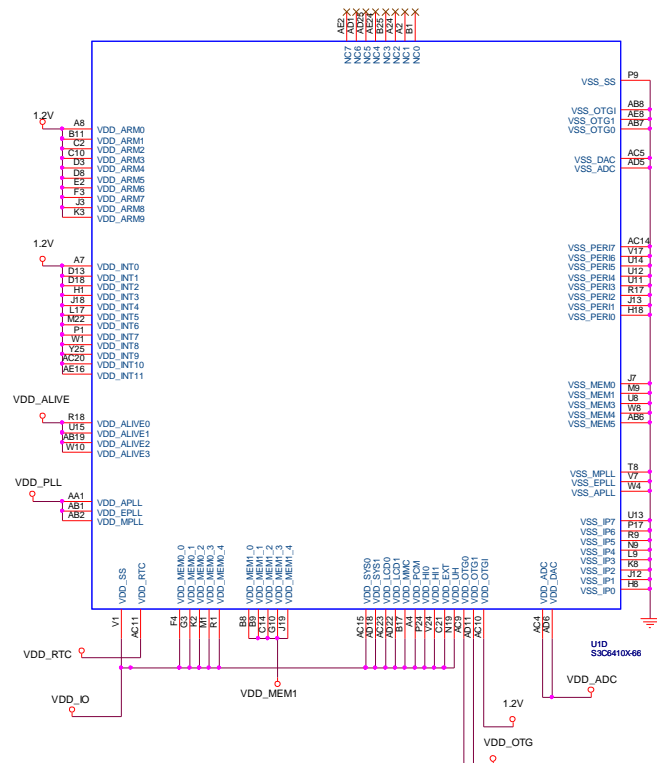
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时钟信号，注意布线尽可能短

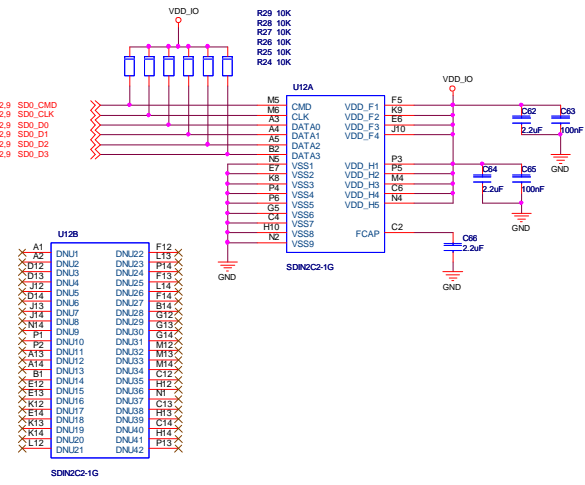
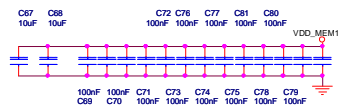
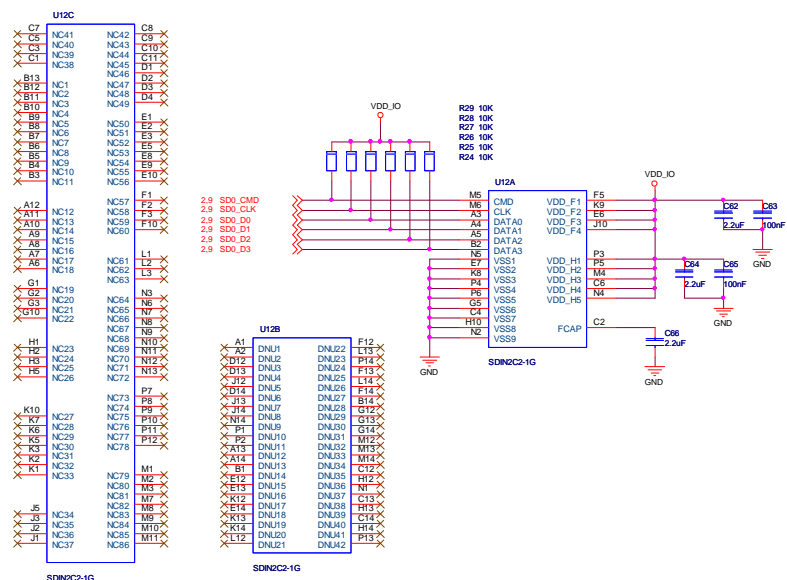
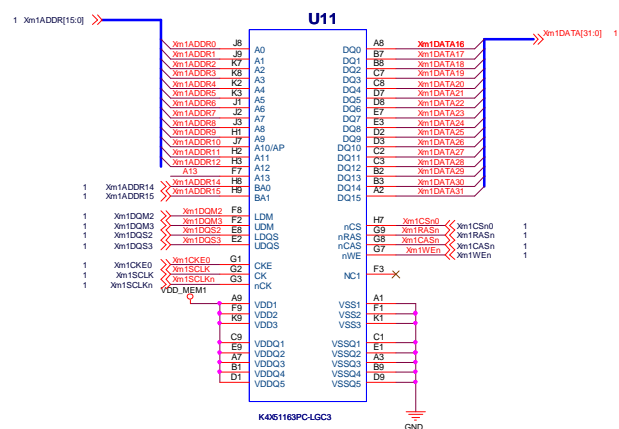
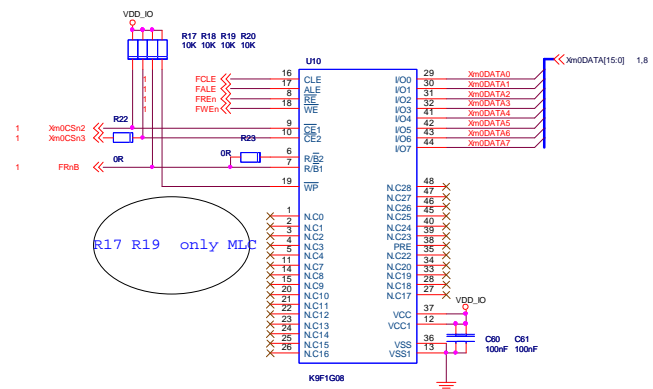
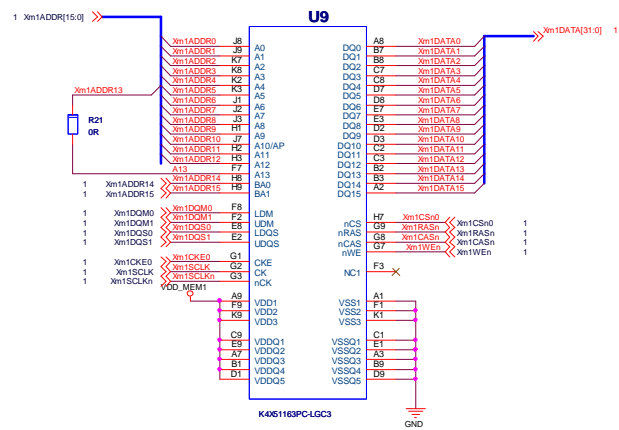
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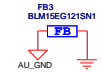
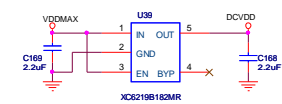
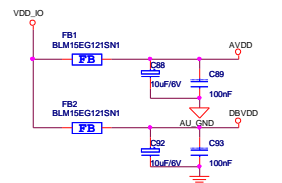
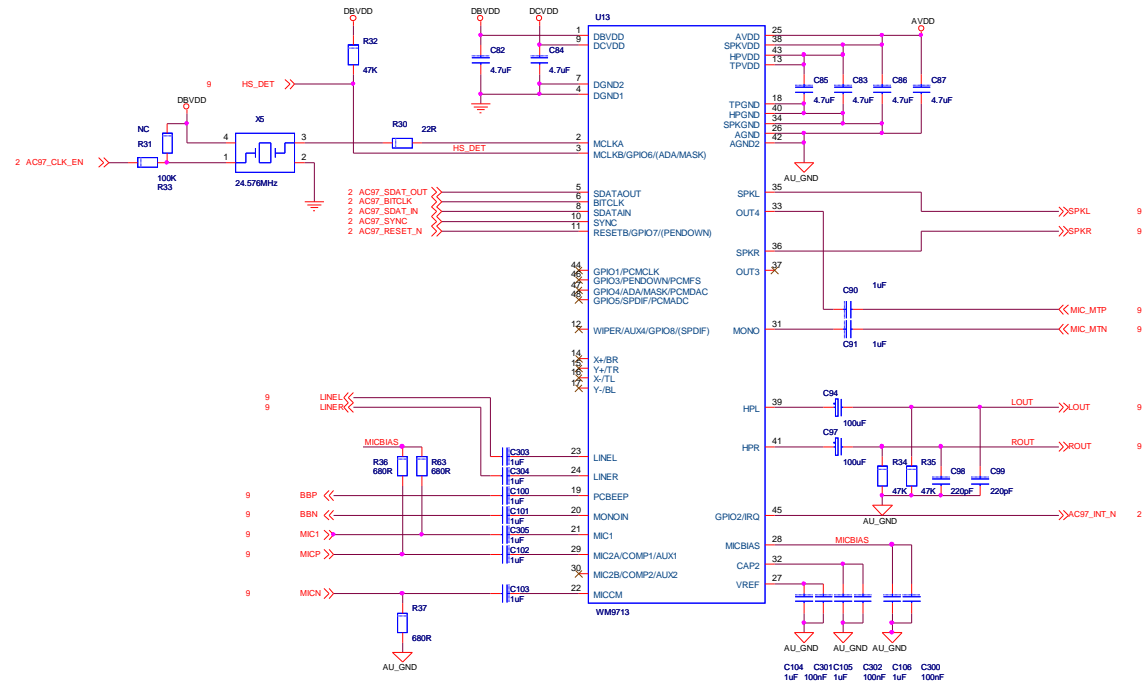
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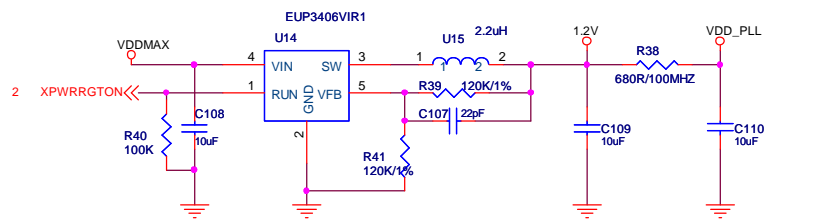
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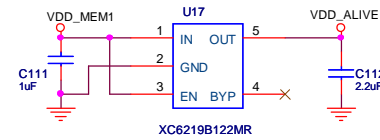


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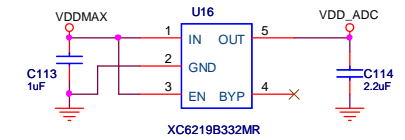
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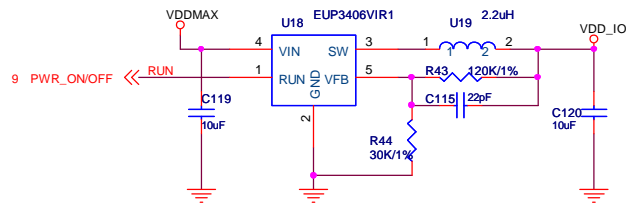
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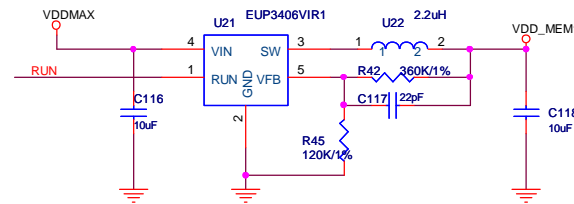
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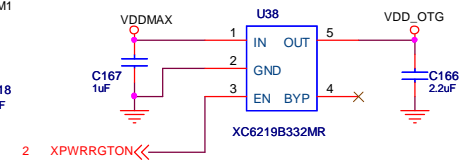
3.3V FOR ADC or DAC



3.3V FOR SYSTEM



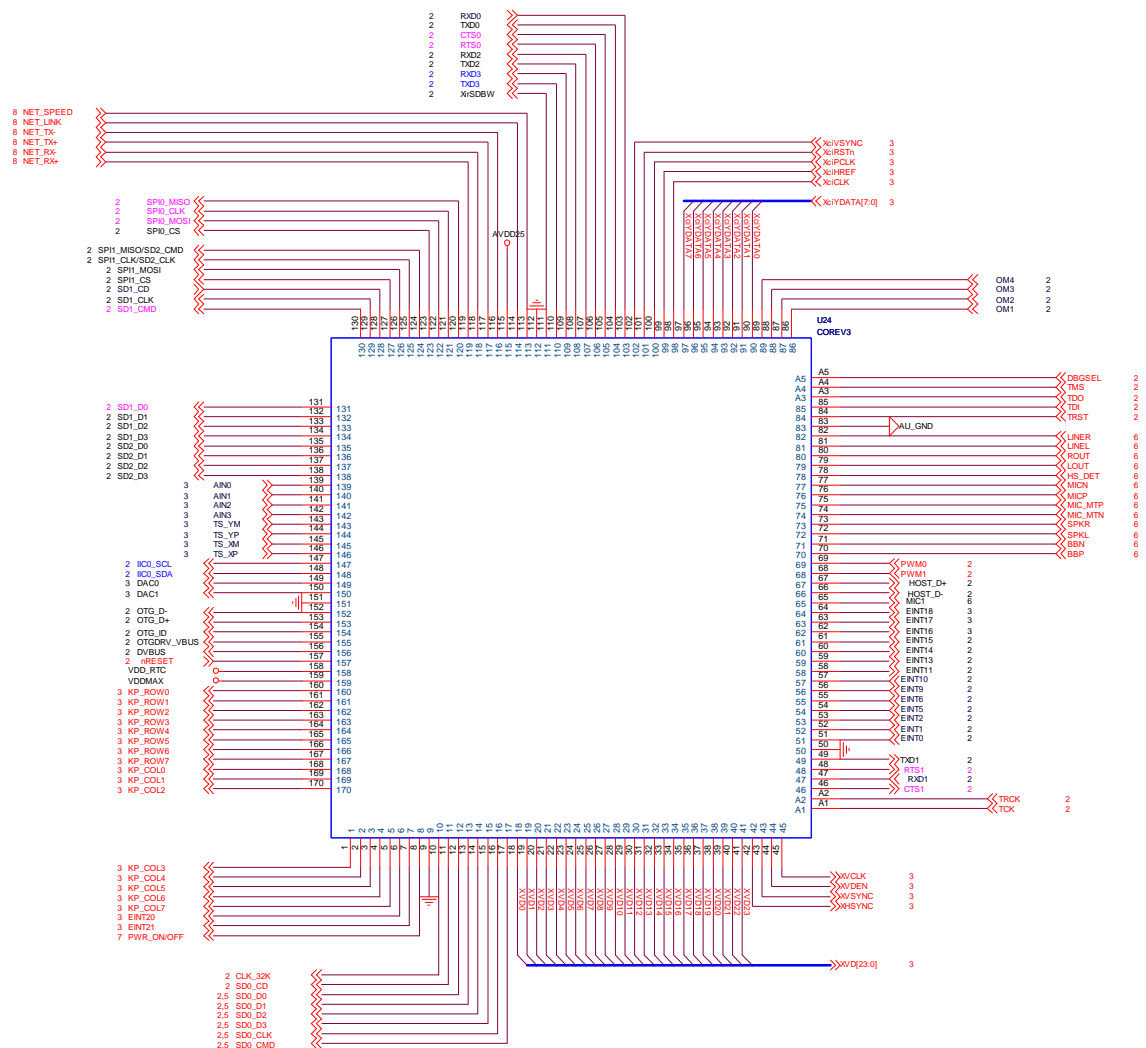
1.8V for mobile DDR



电源和LDO布线，注意线宽线距

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